FORM PTO-●49 INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT	Atty. Docket No. XA-10241	Appln. No. 10/518481
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U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
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FOREIGN PATENT DOCUMENTS

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	AH	WO 91/20028	12/26/91	WIPO			
	AI	2001-56640	02/27/01	Japan			
	AJ	7-50595	02/21/95	Japan			
	AK						
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	AM						
	AN						

OTHER (including author, title, date, pertinent pages, etc.)

	AO	Ramabadran, T. V. et al., "A Tutorial on CRC Computations," IEEE Micro, Vol. 8, No. 4, August 1988, pp. 62-75.
	AP	Moon, S. et al. "Fast VLSI Arithmetic Algorithms for High-Security Elliptic Curve Cryptographic Applications," IEEE Transaction Consumer Electronics, Vol. 47, No. 3, August 2001, pp. 700-708.
	AQ	Mastrovito, E., "VLSI Designs for Multiplication over Finite Fields $GF(2^m)$," Proc. Sixth Int'l Conf. "Applied Alegbra, Algebraic Algorithms, and Error-Connecting Codes (AAECC-6)," July 1988, pp. 297-309.
	AR	Shiba, K. et al., "GF(2^m) Ensan oyobi Seisu Ensan o Shori Kano na Hybrid Coprocessor no Teian," 1999nen Ango to Joho Security Symposium Yokoshu, Volume II of II, January 26, 1999, pp. 819-824.

Examiner /Shahrouz Yousefi/	Date Considered 02/22/2008

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /S.Y./